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PTO/SB/81 (02-01)

Approved for use through 10/31/2002. OMB 0651-0035

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Application Number	10/550,323
Filing Date	September 23, 2005
First Named Inventor	Chamberlain et al.
Title	Intelligent Data Storage and Processing Using FPGA Devices
Group Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	53047/57370

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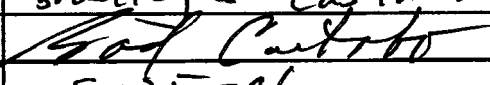
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☐ Applicant/Inventor.

☒ Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).

SIGNATURE of Applicant or Assignee of Record

Name	Bradley J. Castanho
Signature	
Date	8-25-06

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

☒ *Total of 1 forms are submitted.

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